Atty Docket No. HBES 1071-2

Application No. 10/810,312

CERTIFICATE OF ELECTRONIC FILING UNDER 37 CFR 1.8

I hereby certify that this correspondence is being electronically Filed with the U.S. Patent & Trademark Office on 16 April 2007.

/Brianna Dahlberg/

Brianna Dahlbergt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor:

Group Art Unit: 2819

Andrew Martin MALLINSON et al.

Examiner: Brian K. YOUNG

Application No. 10/810,312

Confirmation No. 7978

Filed: 26 March 2004

Customer No. 49413

Title: SYSTEM AND METHOD FOR

COMPENSATING FOR ERROR IN A SIGMA

DELTA CIRCUIT

COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is a Form PTO-SB/08. The Examiner is requested to initial the form and return it to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted.

References submitted with this statement were previously cited in the following U.S. patent documents which deal with the same general subject matter: 7,058,464 issued 06 June 2006; 7,116,257 issued on 03 October 2006; 7,138,935 issued on 21 November 2006; and 11/236,215 filed on 26 September 2005.

This statement should be considered under 37 C.F.R. 1.97(c) because it is being filed before the mailing date of a FINAL office action, a Notice of Allowance, or an action that otherwise closes prosecution in the subject application, whichever occurs first, AND it is accompanied by the \$180 fee set forth in 37 C.F.R. 1.17(p).

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (HBES 1071-2).

Respectfully submitted,

HAYNES BEFFEL & WOLFELD LLP

Date: 16 April 2007 /Ernest J. Beffel, Jr./

> Ernest J. Beffel, Jr. Registration No. 43,489

HAYNES BEFFEL & WOLFELD LLP P.O. Box 366 Half Moon Bay, CA 94019 Telephone: (650) 712-0340

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PTO/SB/08A (08-00)
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Complete if Known 10/810,312 Application Number 26 March 2004 Filing Date First Named Inventor Andrew Martin MALLINSON 2819 Group Art Unit Brian K. YOUNG **Examiner Name**

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary) HBES 1071-2 of Attorney Docket Number Sheet

			U.S. PATENT DOC	JMENTS	
Examiner Initials*	Cite No.1	U.S. Patent Document Kind Code Number (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A1	2004/0165661	Braun	08-26-2004	
	A2	6795004	Masuda et al.	09-21-2004	
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	A13				
	A14				
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FOREIGN PATENT DOCUMENTS											
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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STATEMENT BY APPLICANT				First Named Inventor	Andrew Martin MALLINSON		
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Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the									
Examiner nitials*	Cite No.1	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T						
	C1	JORGE VARONA, "Power Digital-to-Analog Conversion Using Sigma-Delta and Pulse Width Modulations," ECE1371 Analog Electronics II, ECE University of Toronto, pp. 1-14, 2001.							
	C2	ERIK BRESCH and WAYNE T. PADGETT, "TMS320C67-Based Design of a Digital Audio Power Amplifier Introducing Novel Feedback Strategy," Rose-Hulman Institute of Technology, Electrical and Computer Engineering Department, pp. 1-16, 1999.							
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	C8	M. O. J. HAWKSFORD, "Time-Quantized Frequency Modulation, Time-Domain Dither, Dispersive Codes, and Parametically Controlled Noise Shaping in SDM," J. Audio Eng. Soc., Vol. 52, No. 6, pp. 587-617, June 2004. (NOT PRIOR ART)							
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	C10	D. AKSELROD, S. GREENBERG, S. HAVA and V. KOIFMAN, "Noise-Shaped Dynamic Element Matching Architecture for Multibit Delta-Sigma Converters," Accepted for publication in: Proceedings of Communication Systems, Networks and Digital Signal Processing, July 2002.							
	C11	E. BACH, "Multibit Oversampling D/A converters Using Dynamic Element Matching Methods," Siemens AG, Semiconductors Group, March 3, 1999.							

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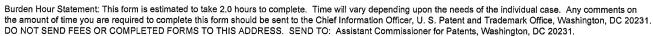
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xaminer Cite item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s)		September 1	OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the	T
Quantization in Delta-Sigma ADCs," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 2, February 2001. C13 Y. GEERTS and M. STEYAERT, "Guidelines for Implementation of CMOS Multibit Oversampling Modulators," K.U. Leuven, ESAT-MICAS, January 1999. C14 H. JENSEN, "A Low-Complexity Dynamic Element Matching DAC for Direct Digital Synthesis,"	Examiner Initials*	No.1	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
Oversampling Modulators," K.U. Leuven, ESAT-MICAS, January 1999. C14 H. JENSEN, "A Low-Complexity Dynamic Element Matching DAC for Direct Digital Synthesis,"			Quantization in Delta-Sigma ADCs," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 2, February 2001.	
C14 H. JENSEN, "A Low-Complexity Dynamic Element Matching DAC for Direct Digital Synthesis," IEEE Transactions on Circuits and Systems-II: Signal Processing, Vol. 45, No.1, January 1998.		C13	Y. GEERTS and M. STEYAERT, "Guidelines for Implementation of CMOS Multibit	
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